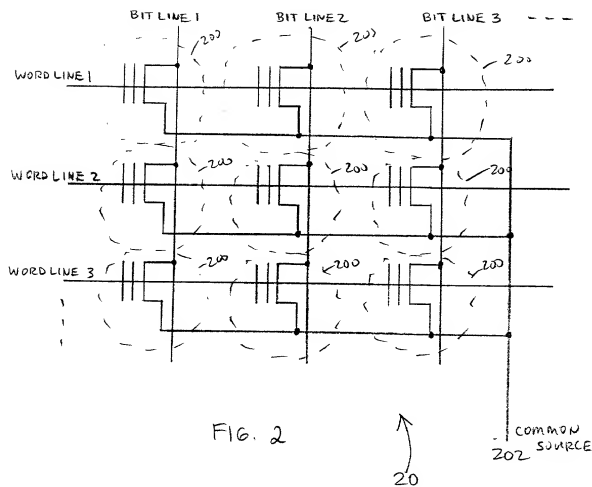


FIG. 1



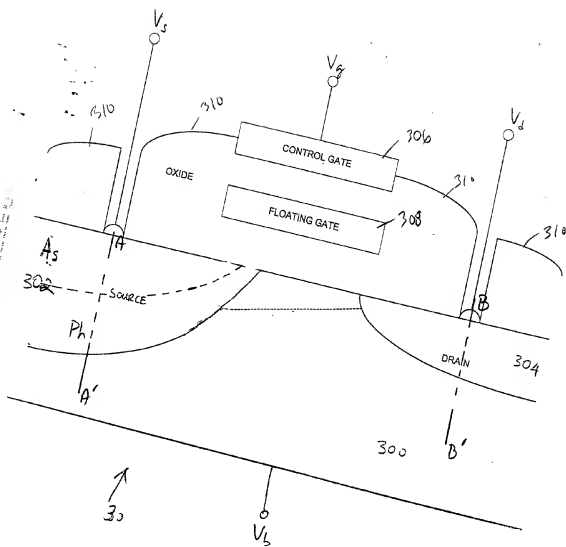
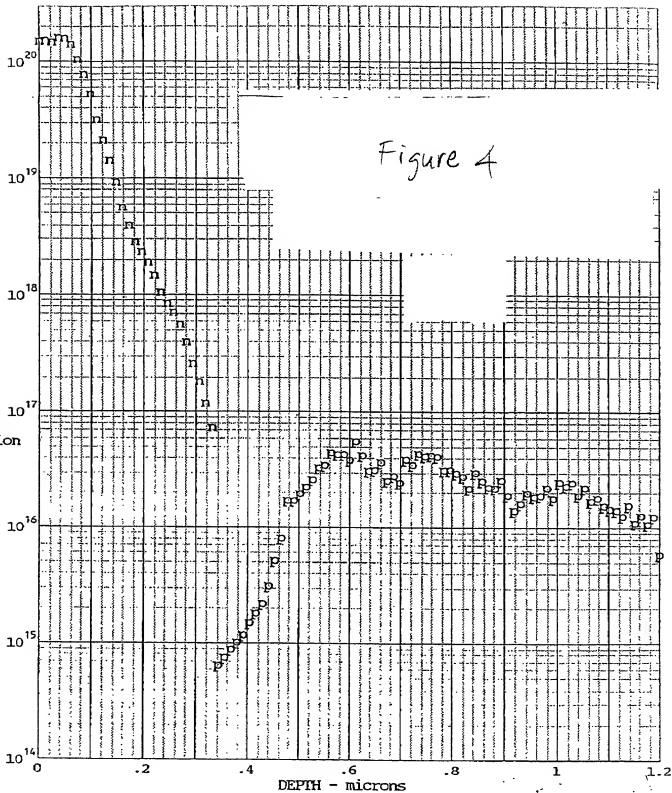


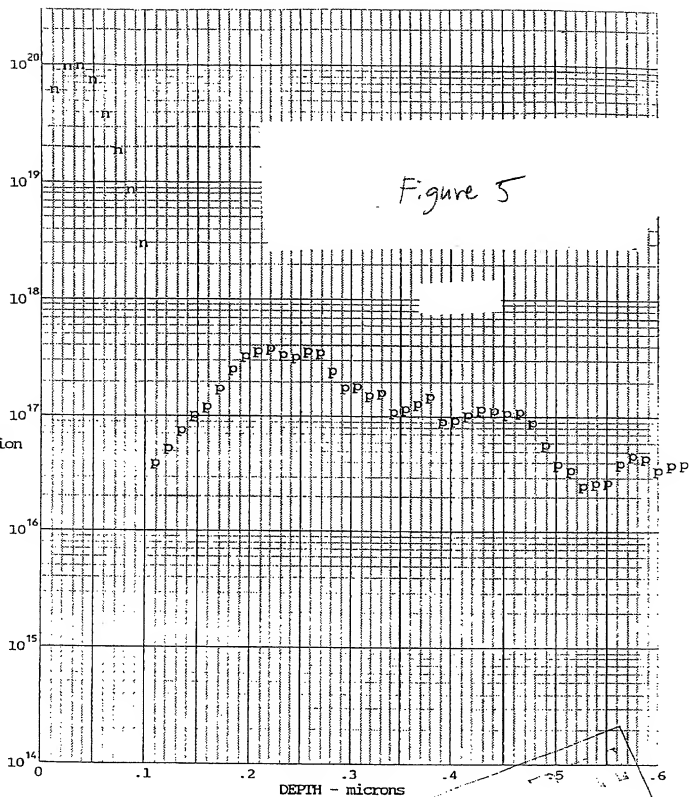
FIG. 3

Carrier
Concentration
(cm^{-3})

Figure 4



Carrier
Concentration
(cm^{-3})



Drain Side Program Rate Distribution After UV Erase

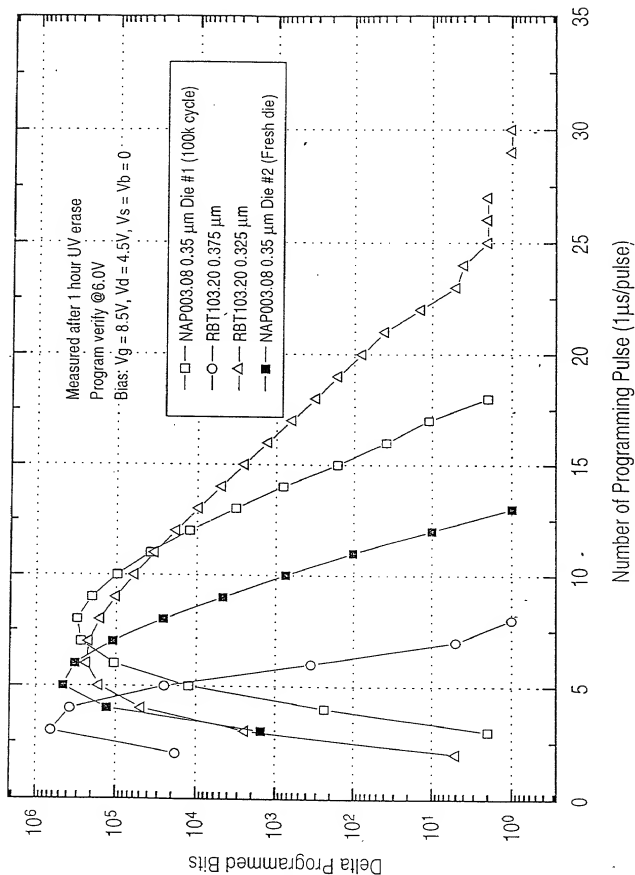


FIG. 6

Source Side Program Rate Distribution After UV Erase

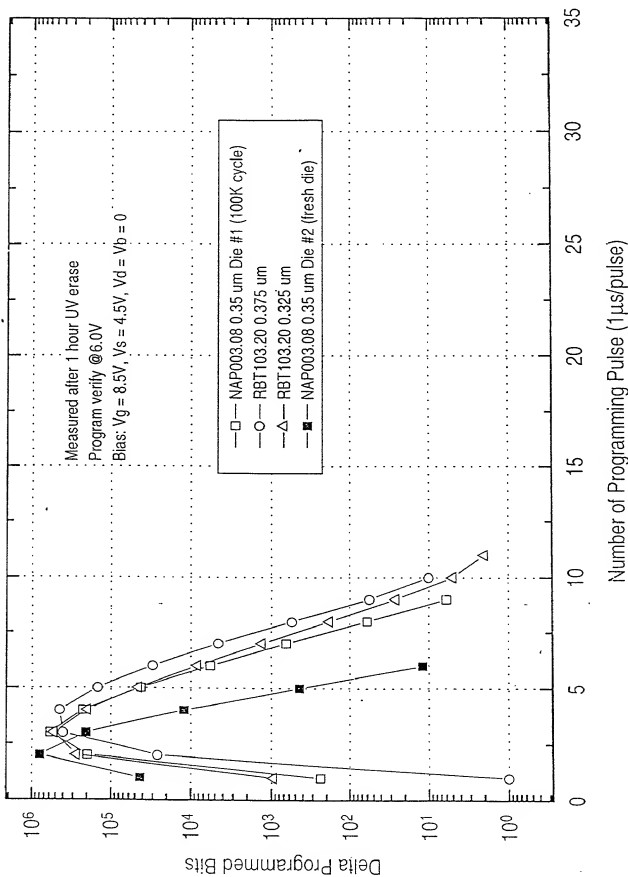
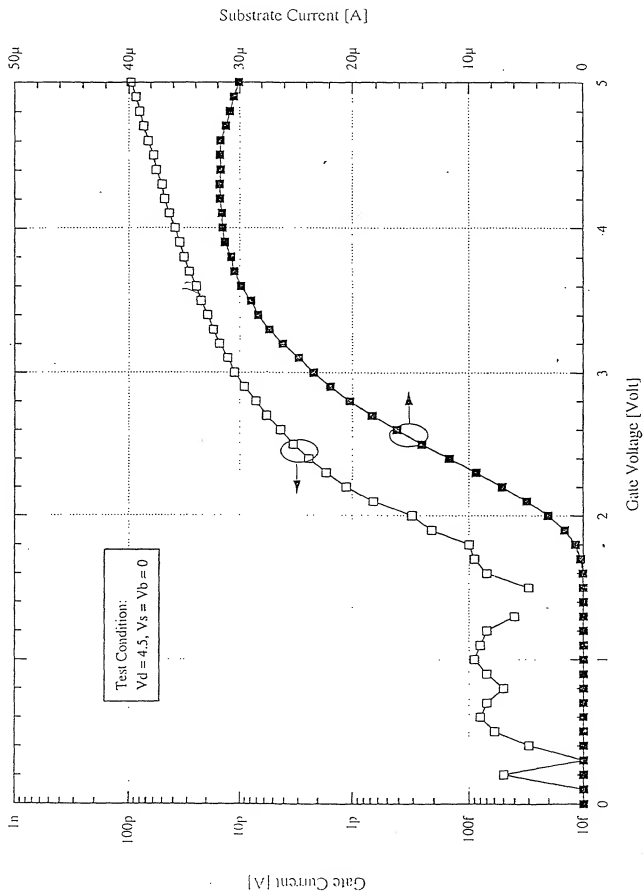


Fig.7

Gate / Substrate Current Characteristics



F16.8

Gate / Substrate Current Characteristics

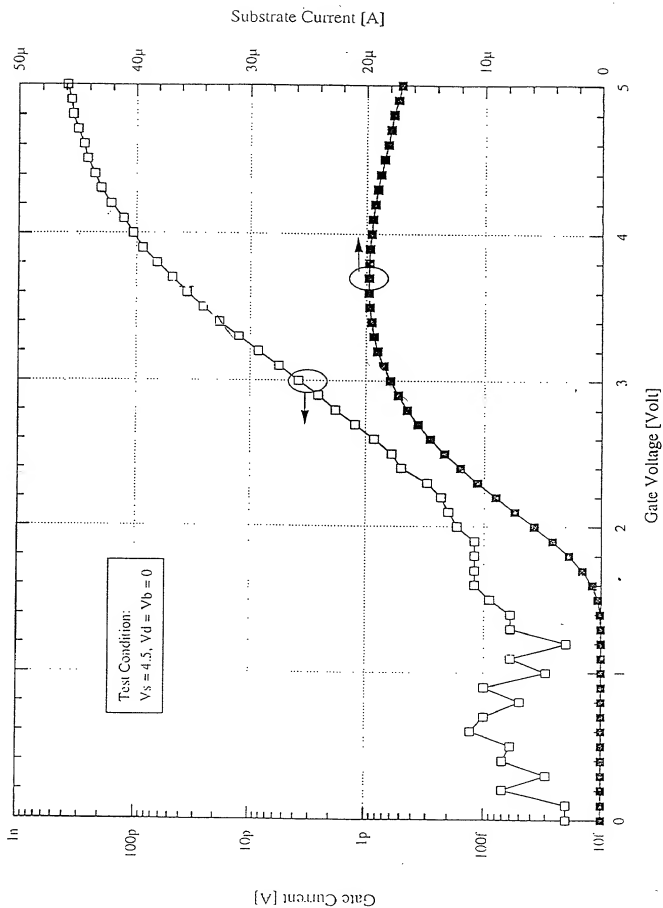


FIG. 9